Page 1 of 9

Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

1/9

FIG.1

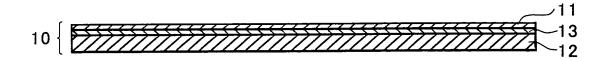


FIG.2

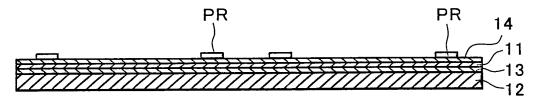
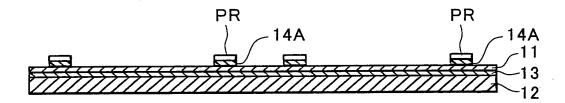


FIG.3



Page 2 of 9

Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

2/9

FIG.4

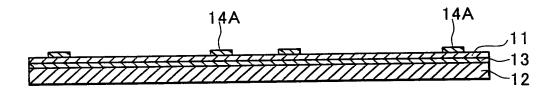


FIG.5

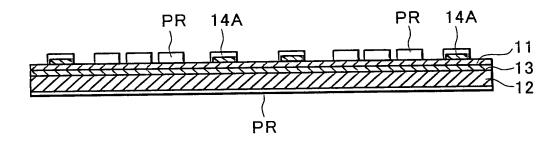
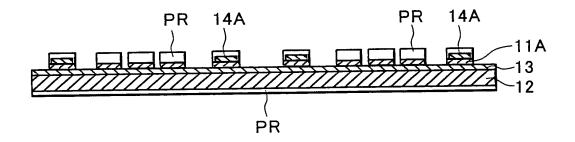


FIG.6



Page 3 of 9

Matter No.: 14225-026001

Applicant(s): Yusuke Igarashi et al.
METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.7

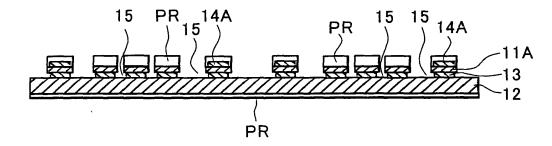


FIG.8

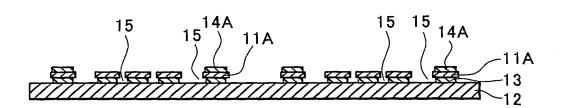
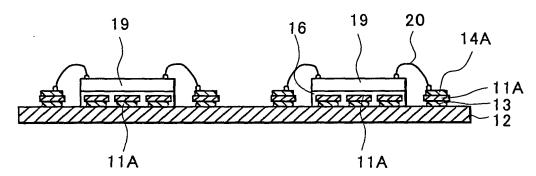


FIG.9



Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.10

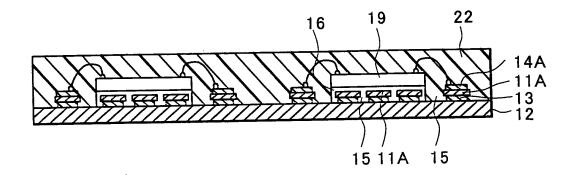


FIG.11

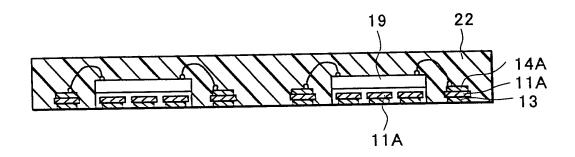
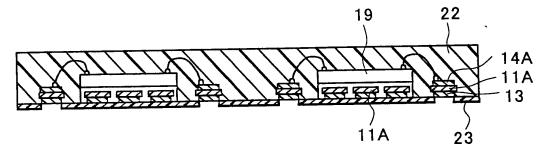


FIG.12



Page 5 of 9

Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.13

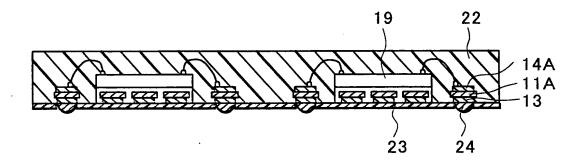
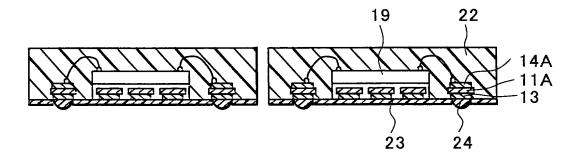
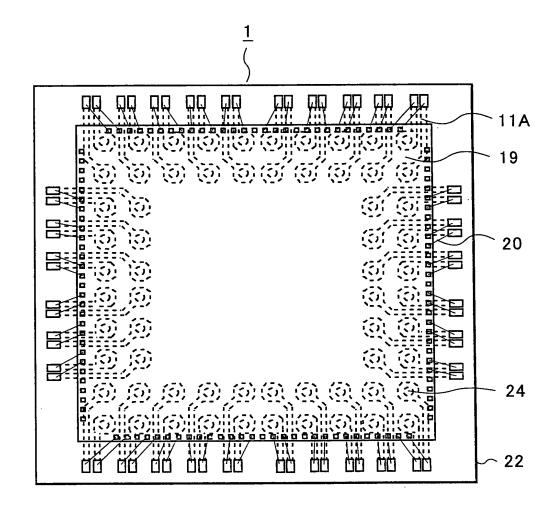


FIG.14



Applicant(s): Yusuke Igarashi et al.
METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.15



Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

7/9

FIG.16

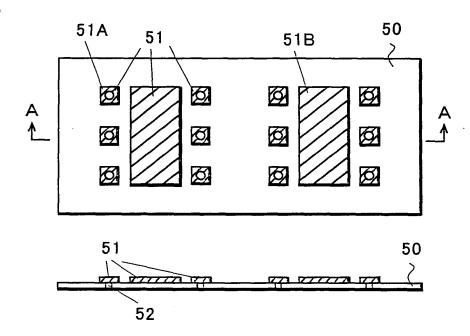
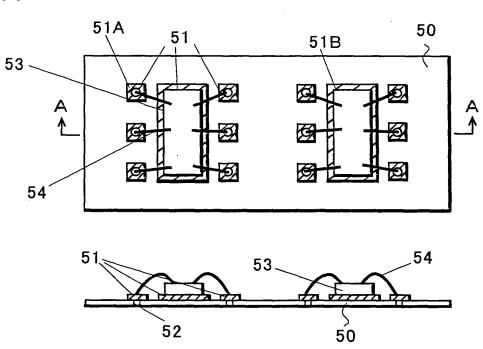


FIG.17



Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.18A

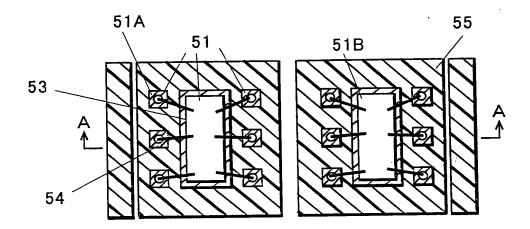


FIG.18B

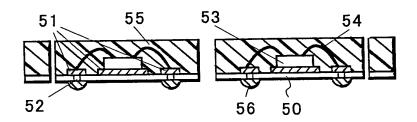
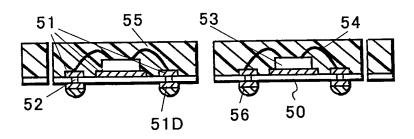


FIG.18C



Matter No.: 14225-026001 Page Applicant(s): Yusuke Igarashi et al. METHOD FOR MANUFACTURING CIRCUIT DEVICES

FIG.19

